

United States Patent and Trademark Office



UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO	Э.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/662,447		09/16/2003	Jang-Hyoun Youm	1572.1164	2512	
21171	7590	02/28/2005	EXAMINER			
STAAS & HALSEY LLP SUITE 700 1201 NEW YORK AVENUE, N.W.				PATEL, RAJ	PATEL, RAJNIKANT B	
				ART UNIT	PAPER NUMBER	
		DC 20005		2838		
				DATE MAILED: 02/28/2005	DATE MAILED: 02/28/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/662,447	YOUM, JANG-HYOUN				
Office Action Summary	Examiner	Art Unit				
	Rajnikant B. Patel	2838				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
2a) ☐ This action is FINAL . 2b) ☐ This 3) ☐ Since this application is in condition for allowar	·—					
Disposition of Claims						
4) Claim(s) 1-21 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1-21 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
 9) The specification is objected to by the Examiner. 10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152. 						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 9/16/03.	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:					

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) The invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 14-21 are rejected under 35 U.S.C. 102(b) as being anticipated by Yamano et al. (U.S. Patent # 4,750,102).

Yamano et al. disclose the claimed invention a motor power supply (figure 2), including a DC conversions part (figure 2, item 7), an over voltage protection switching (figure 2, item 8 and 9), a sensor (figure 2, item 12), and a controller (column 6, line 5-30), a pair of capacitor (figure 2, item 5 and 6), a pair of field effect transistor (figure 2, item 8 and 9), a comparator (figure 2, item 19).

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Art Unit: 2838

2. Claims 1-13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamano et al. (U.S. Patent # 4,750,102) in combination with Miura et al. (U.S. Patent # 6,278,910).

Yamano et al. disclose the claimed invention a motor power supply (figure 2), including a DC conversion part (figure 2, item 7), an over voltage protection switching (figure 2, item 8 and 9), a sensor (figure 2, item 12), and a controller (column 6, line 5-30), a pair of capacitor (figure 2, item 5 and 6), a pair of field effect transistor (figure 2, item 8 and 9), a comparator (figure 2, item 19). However Yamano et al. does not disclose the utilization of the technique for an inrush current limiting resistance and a relay selectively bypass the limiting resistance. Miura et al. teaches the utilization of the similar technique for an inrush current limiting resistance and a relay selectively bypass the limiting resistance (figure 2, item 21 and 22). It would have been obvious one having an ordinary skill in the art at the time the invention was made to modify Yamano et al.'s power supply by utilizing the technique taught by Miura et al. for the purpose of improving operating efficiency and protection to circuit component.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Rajnikant B. Patel whose telephone number is 571-272-2082. The examiner can normally be reached on 6.30-5.00; m-f.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on 703-308-1680. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Application/Control Number: 10/662,447

Art Unit: 2838

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Rajnikant B Patel Primary Examiner Art Unit 2838

fairs b. lan

Page 4
